

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

#29  
MR

Applicant(s): Mohsen, Amr M.  
Assignee: Aptix Corporation  
Title: FIELD PROGRAMMABLE PRINTED CIRCUIT BOARD  
Serial No.: 08/632,298 Filed: 4/12/96  
Batch No.: J55 Date Allowed: 3/13/00  
Examiner: H. Jones Group Art Unit: 2763  
Docket No.: M-1007-6C US



San Jose, California  
May 19, 2000

BOX ISSUE FEE

Attn: Official Draftsperson  
ASSISTANT COMMISSIONER FOR PATENTS  
Washington, D. C. 20231

**SUBMISSION OF FORMAL DRAWINGS**

Sir:

Enclosed for the above patent application are twenty (20) sheets of formal drawings. The enclosed formal drawings incorporate the changes requested in the preliminary amendment, dated 12 April 1996, to the drawings, and are believed to conform to the PTO Form 948 attached to Paper No. 4. If the enclosures are acceptable, please substitute them for the informal drawings now on file. If there are any questions regarding these drawings, please telephone Applicant's attorney at 408-453-9200, ext. 1371.

I hereby certify that this correspondence is being deposited with the United States Postal Service as First Class Mail in an envelope addressed to: Assistant Commissioner for Patents, Washington, D.C. 20231, on May 19, 2000.

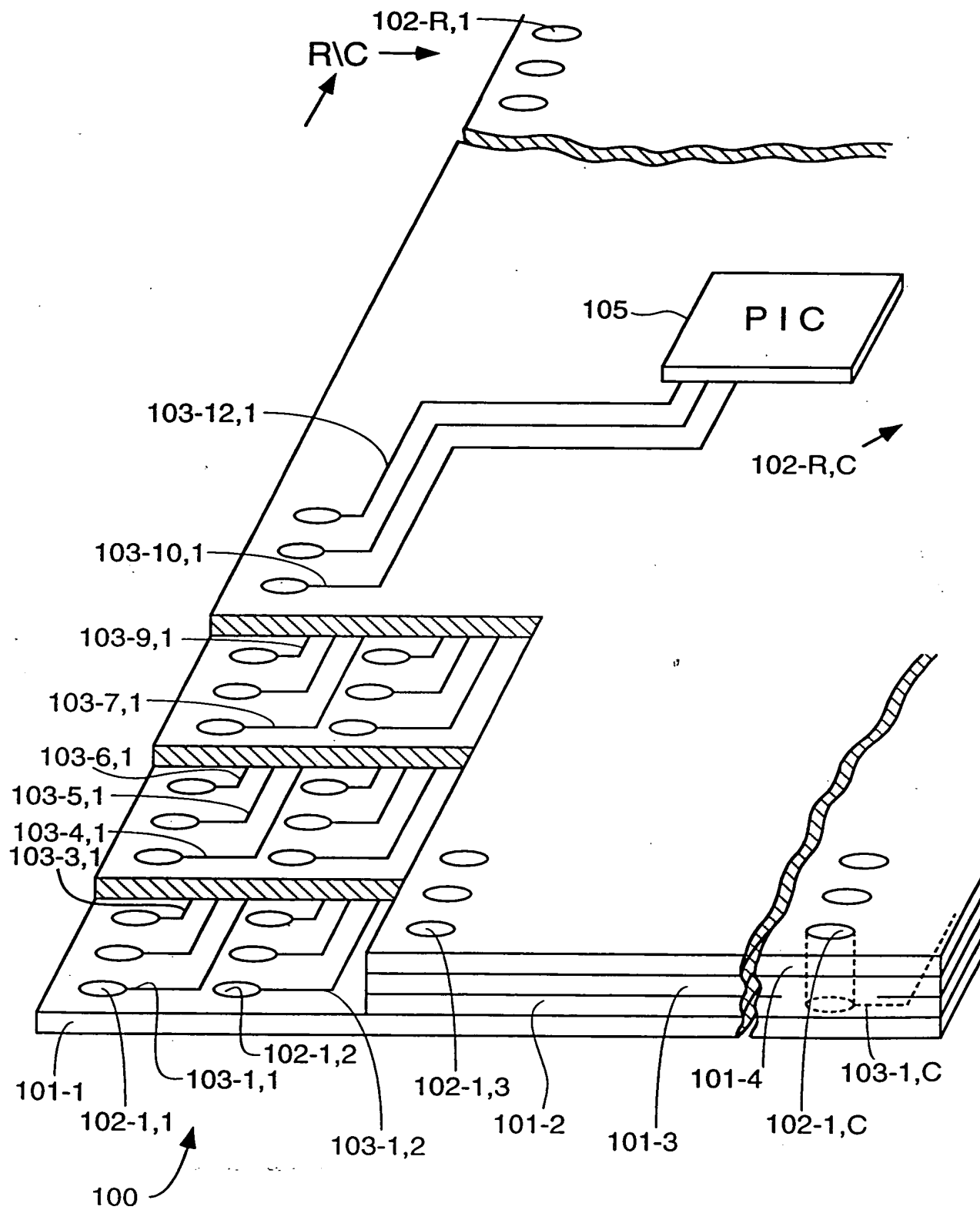
Ronald J. Meetin 19 May 2000  
Attorney for Applicant(s) Date of Signature

Respectfully submitted,

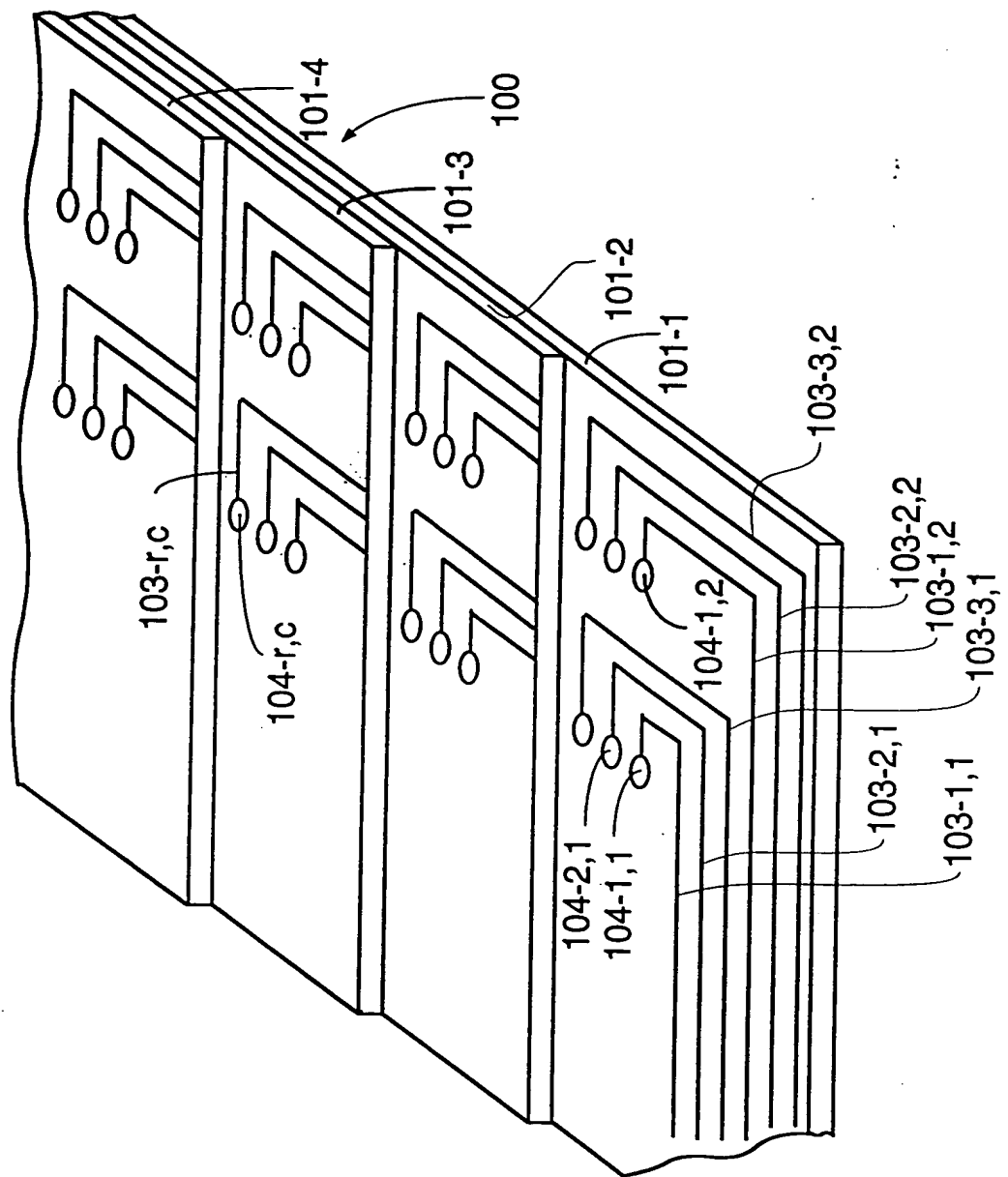
*Ronald J. Meetin*

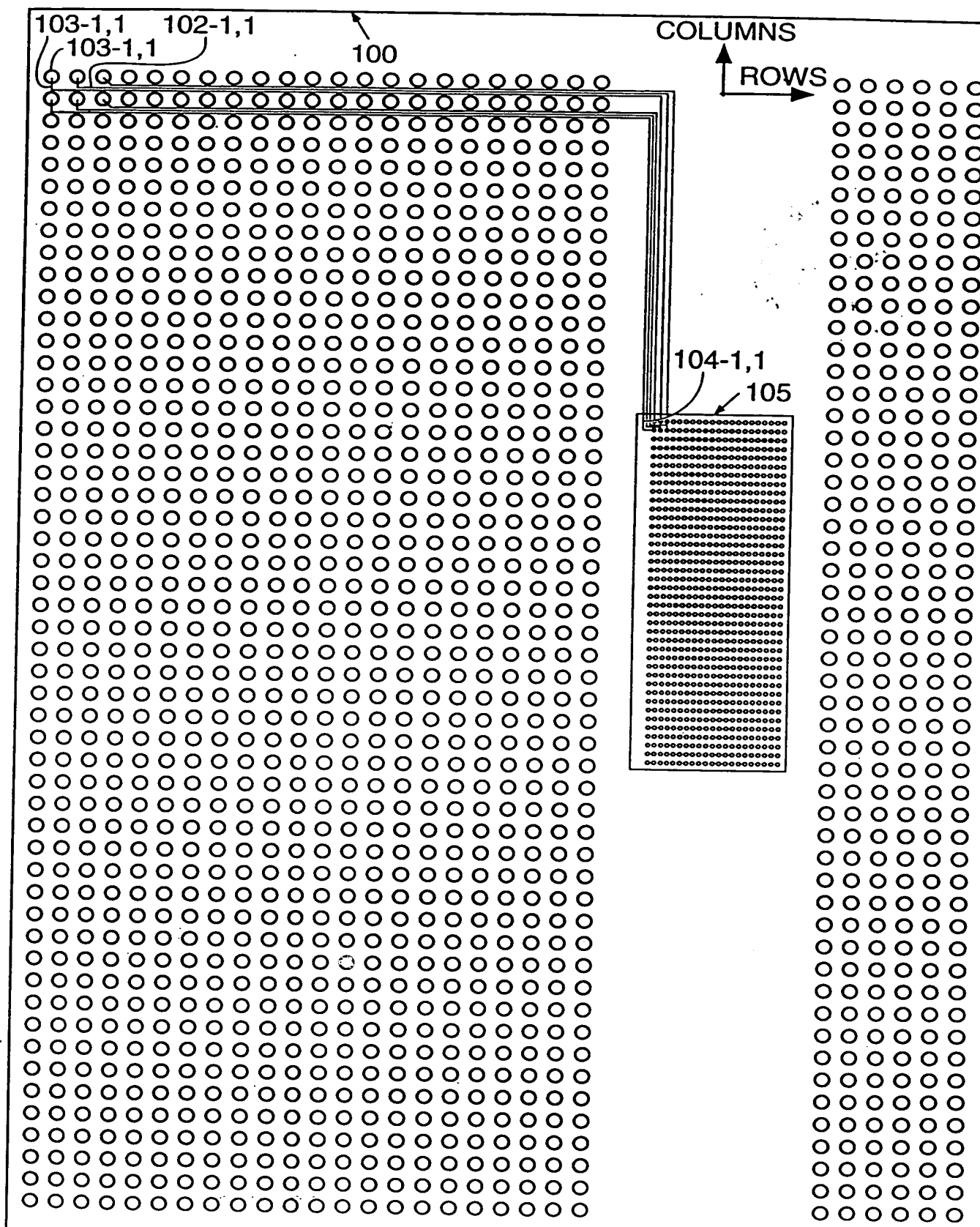
Ronald J. Meetin  
Attorney for Applicant(s)  
Reg. No. 29,089

**FIGURE 1a**

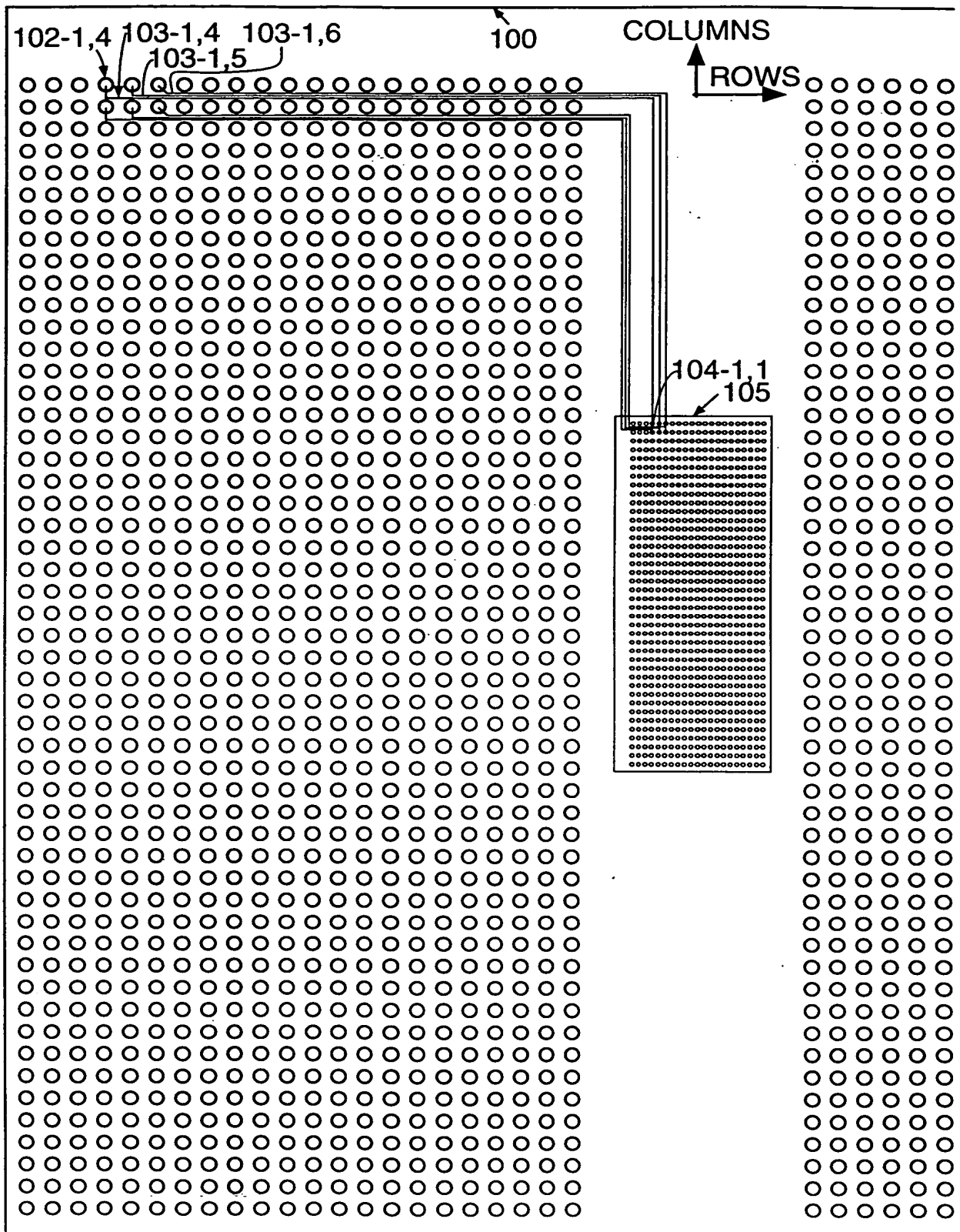


**FIGURE 1b**

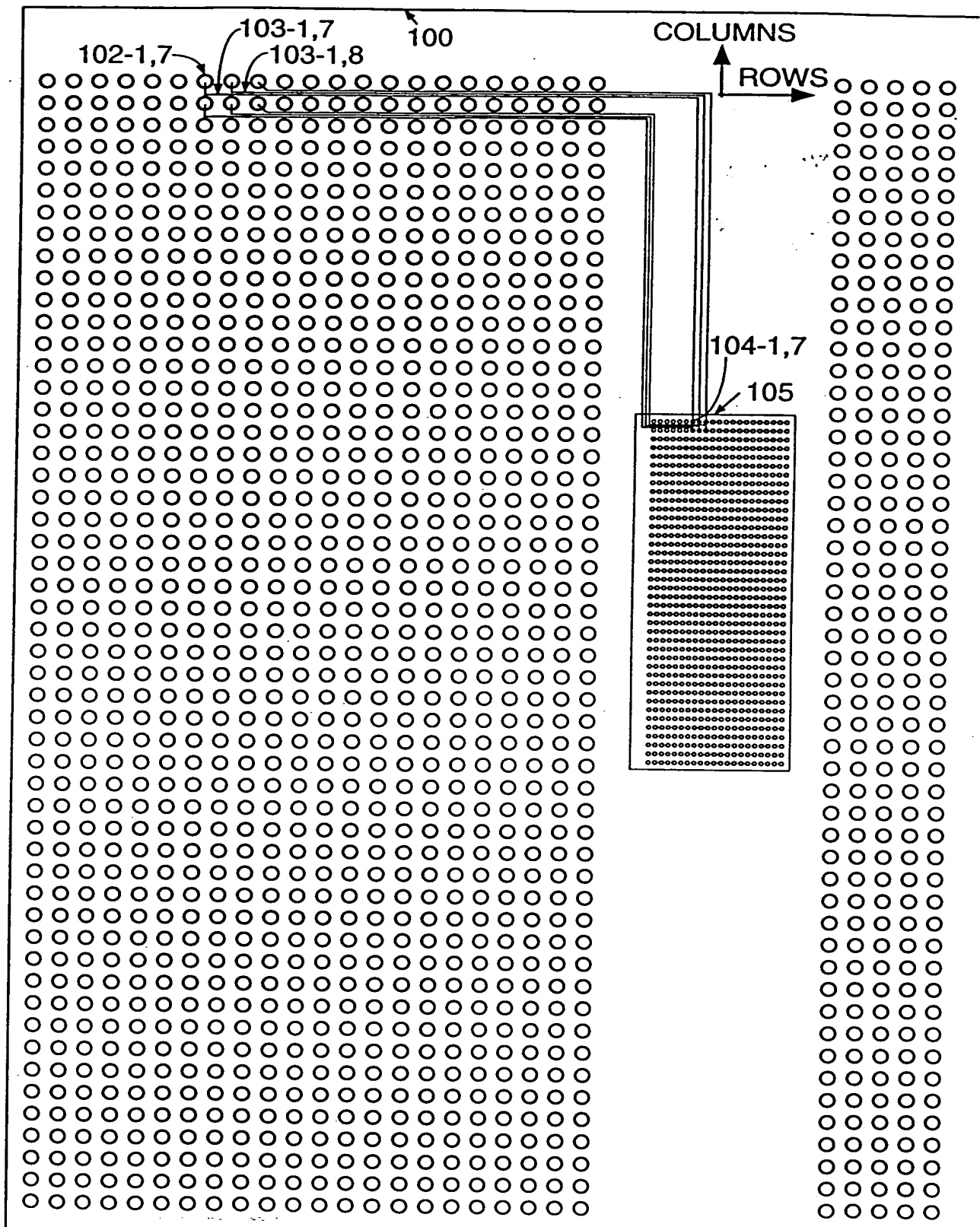




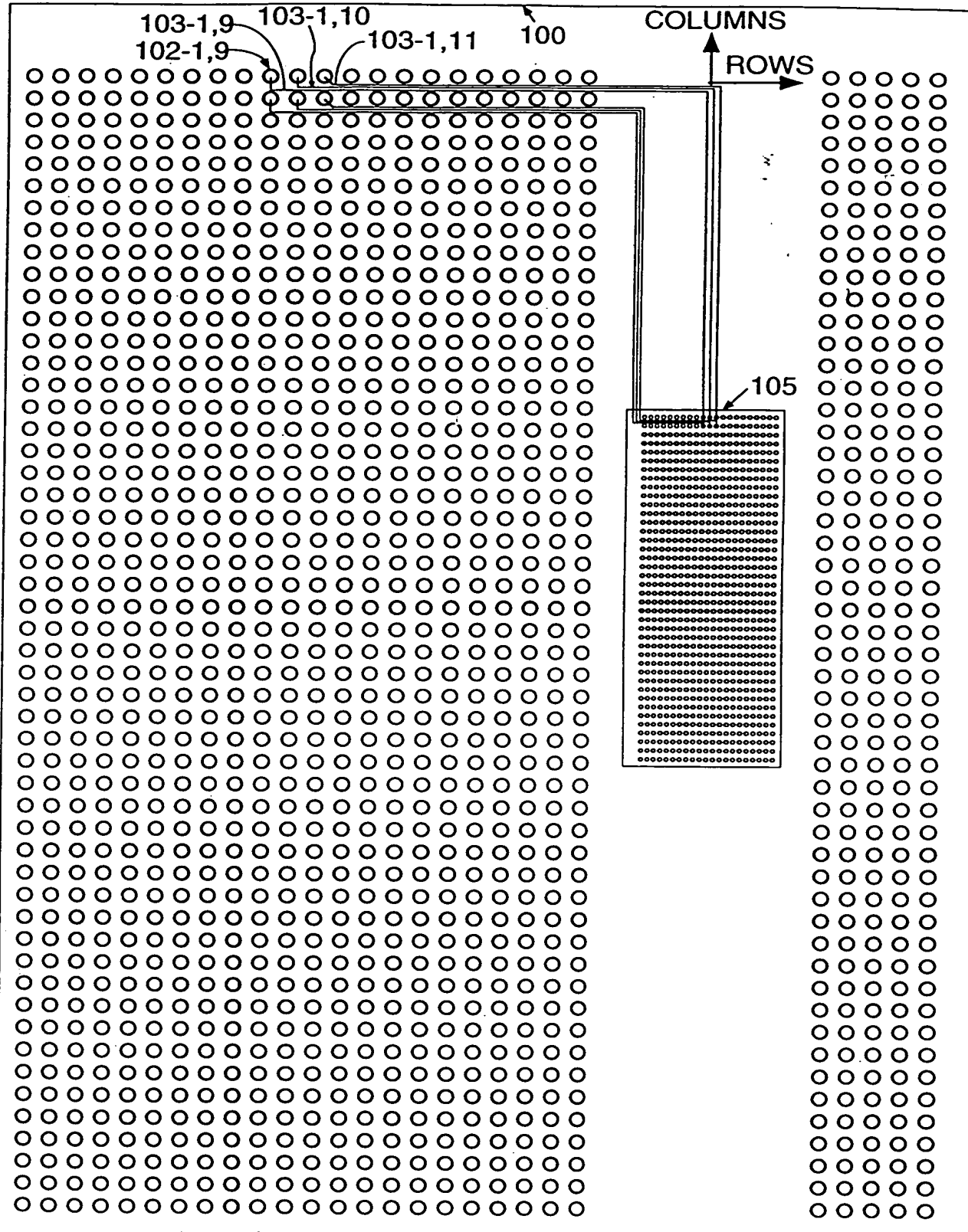
**FIGURE 2a**



**FIGURE 2b**



**FIGURE 2c**



**FIGURE 2d**

# GLOBAL INTERCONNECT ARCHITECTURE

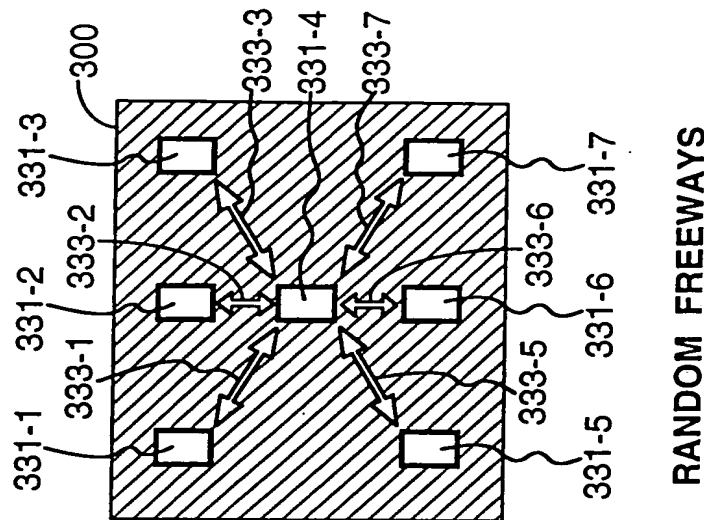
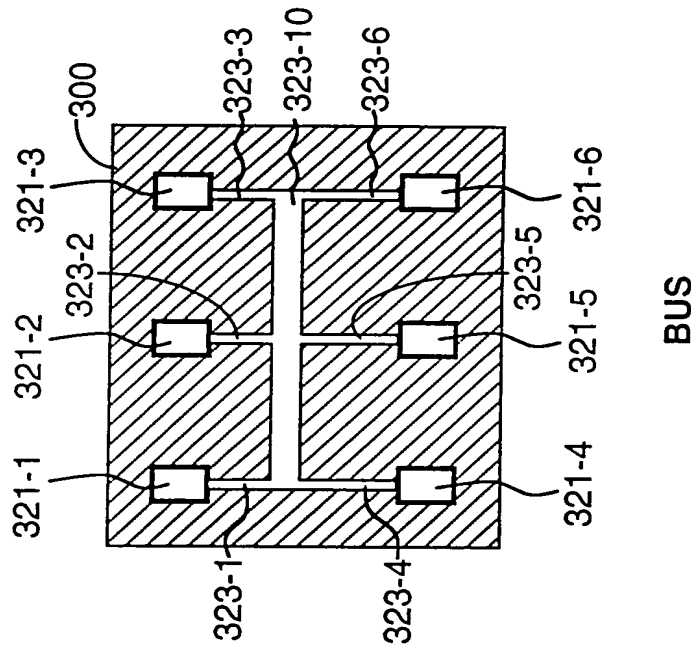
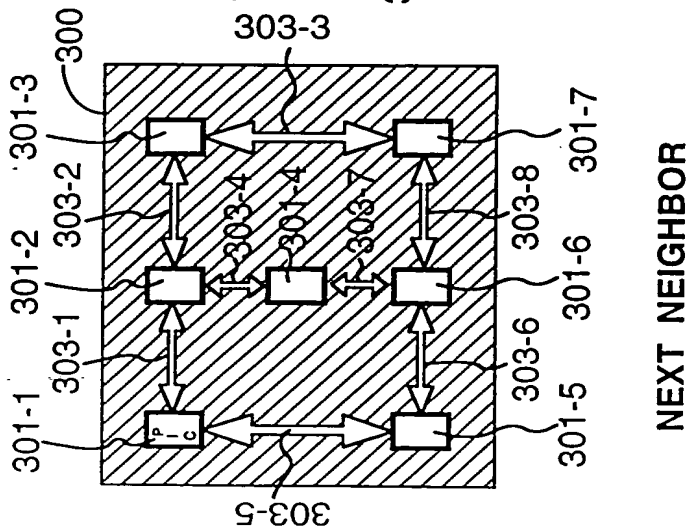


FIGURE 3a

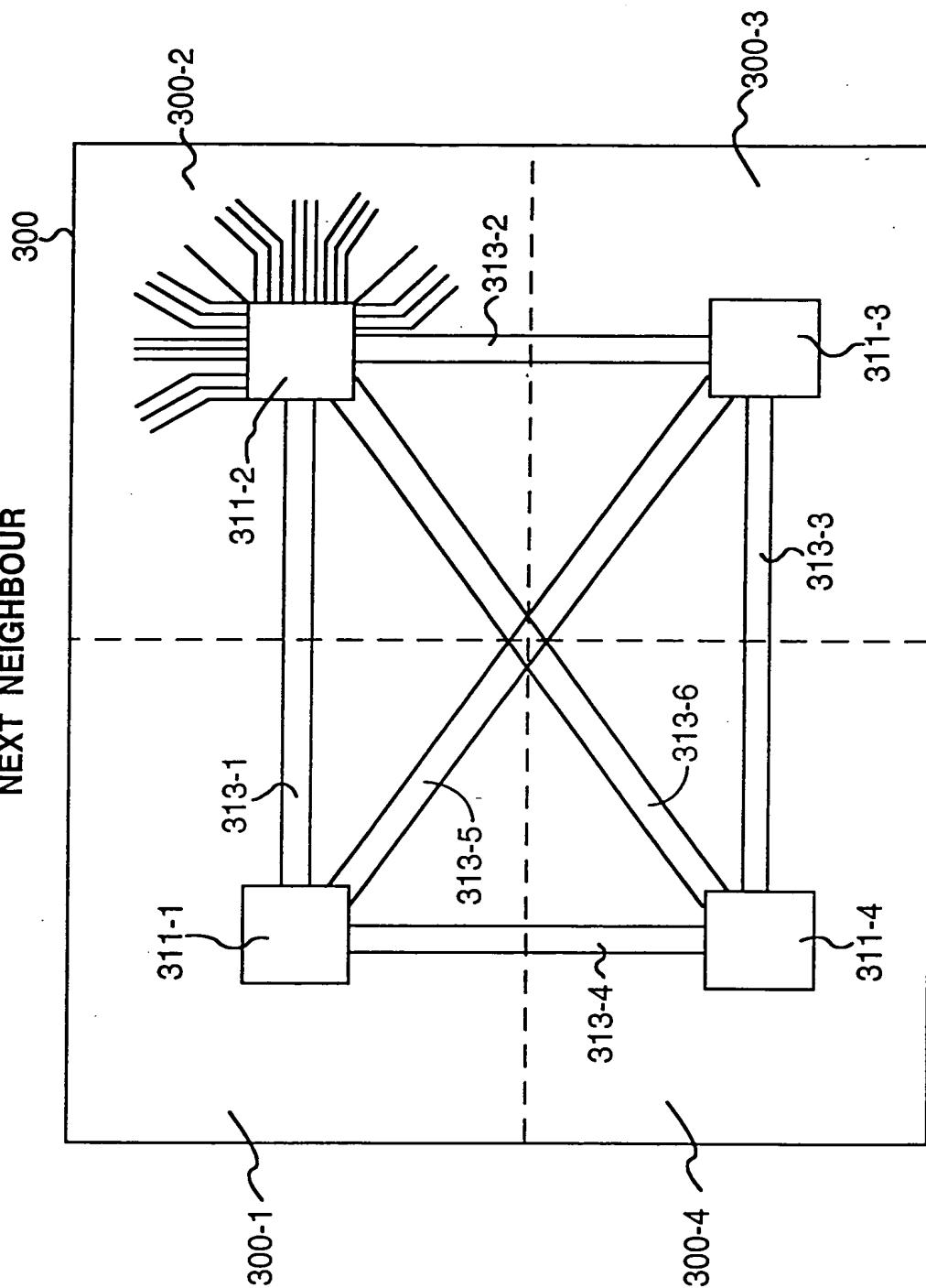
FIGURE 3c

FIGURE 3d



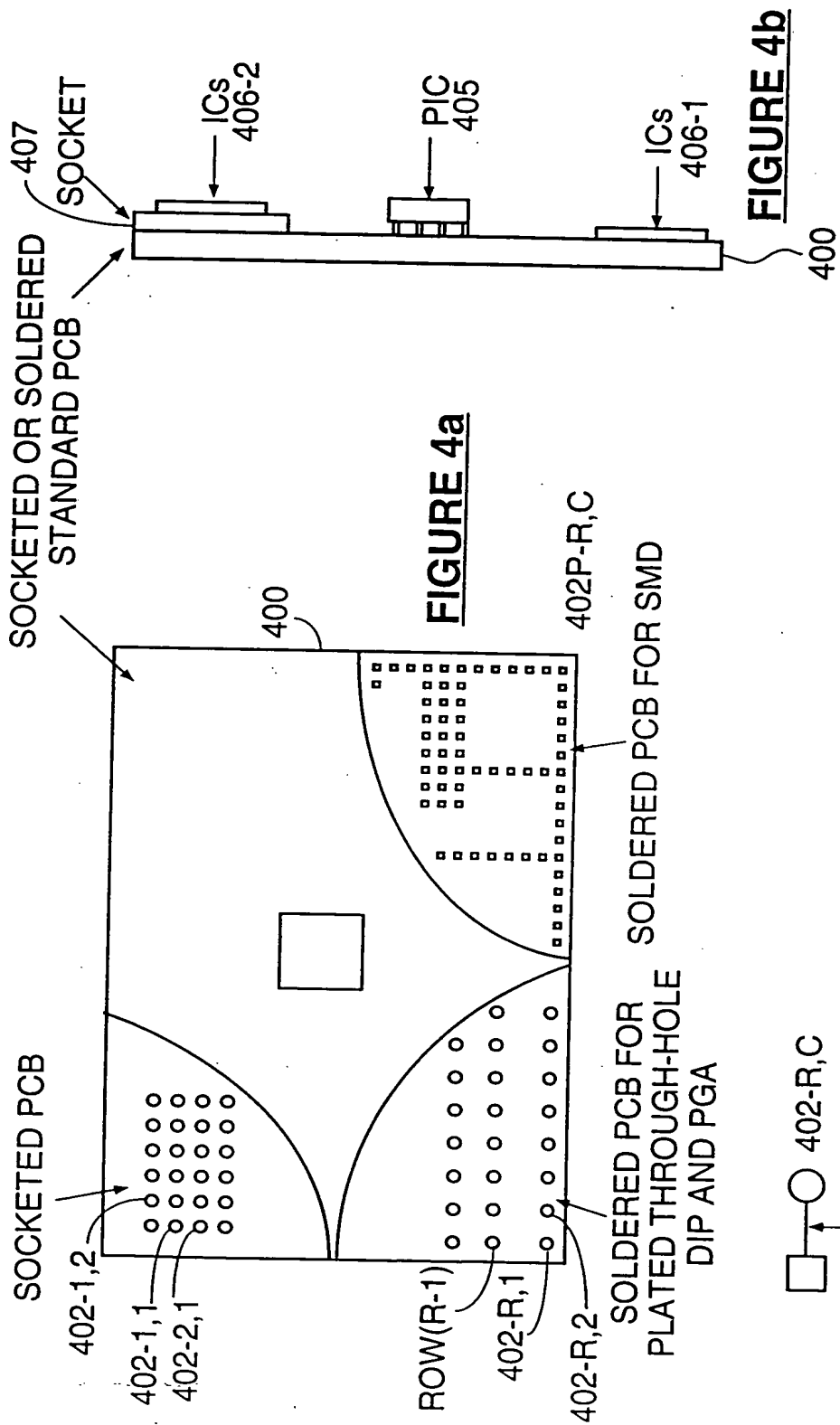
# HIERARCHY OF GLOBAL INTERCONNECTS

NEXT NEIGHBOUR

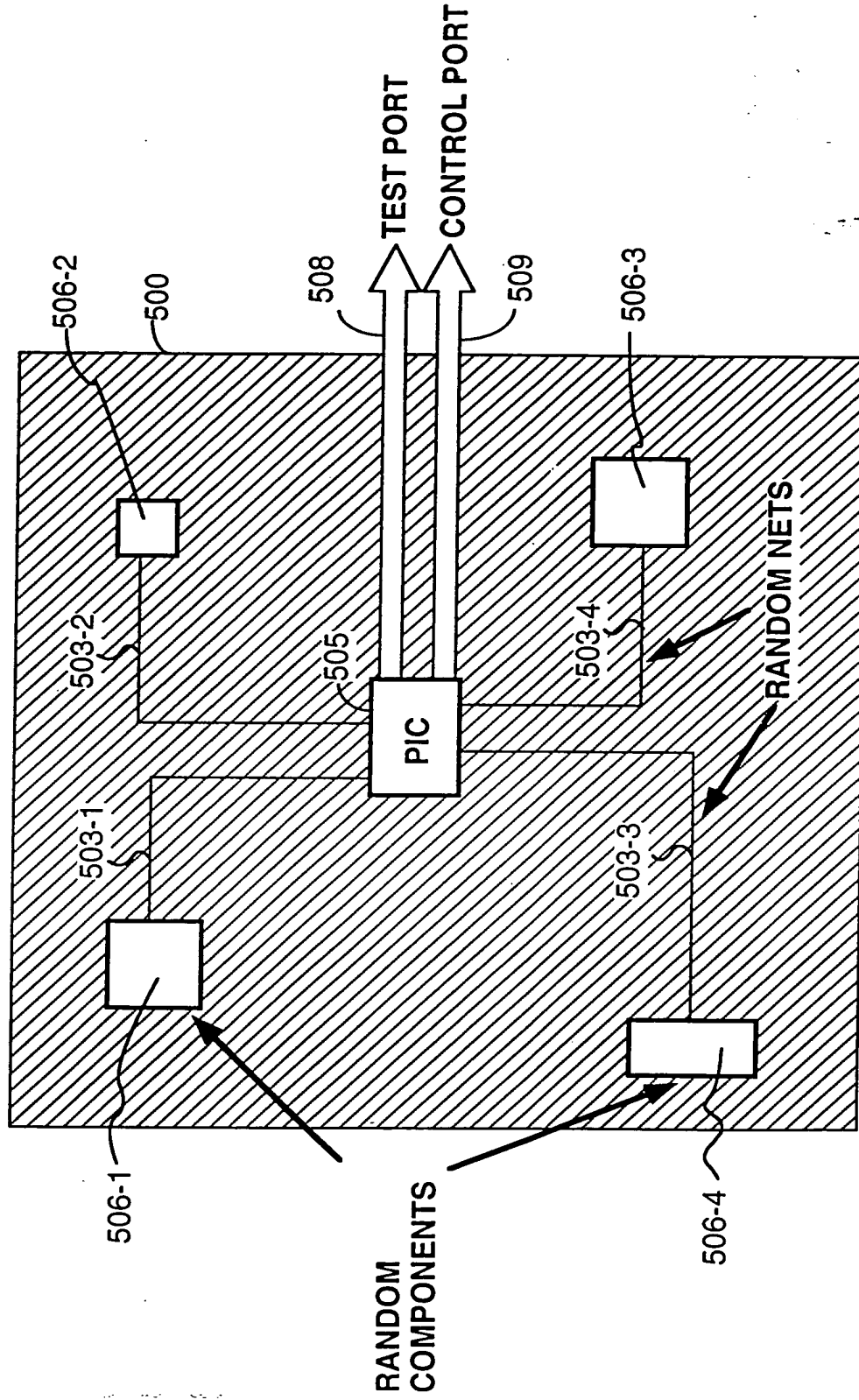


**FIGURE 3b**

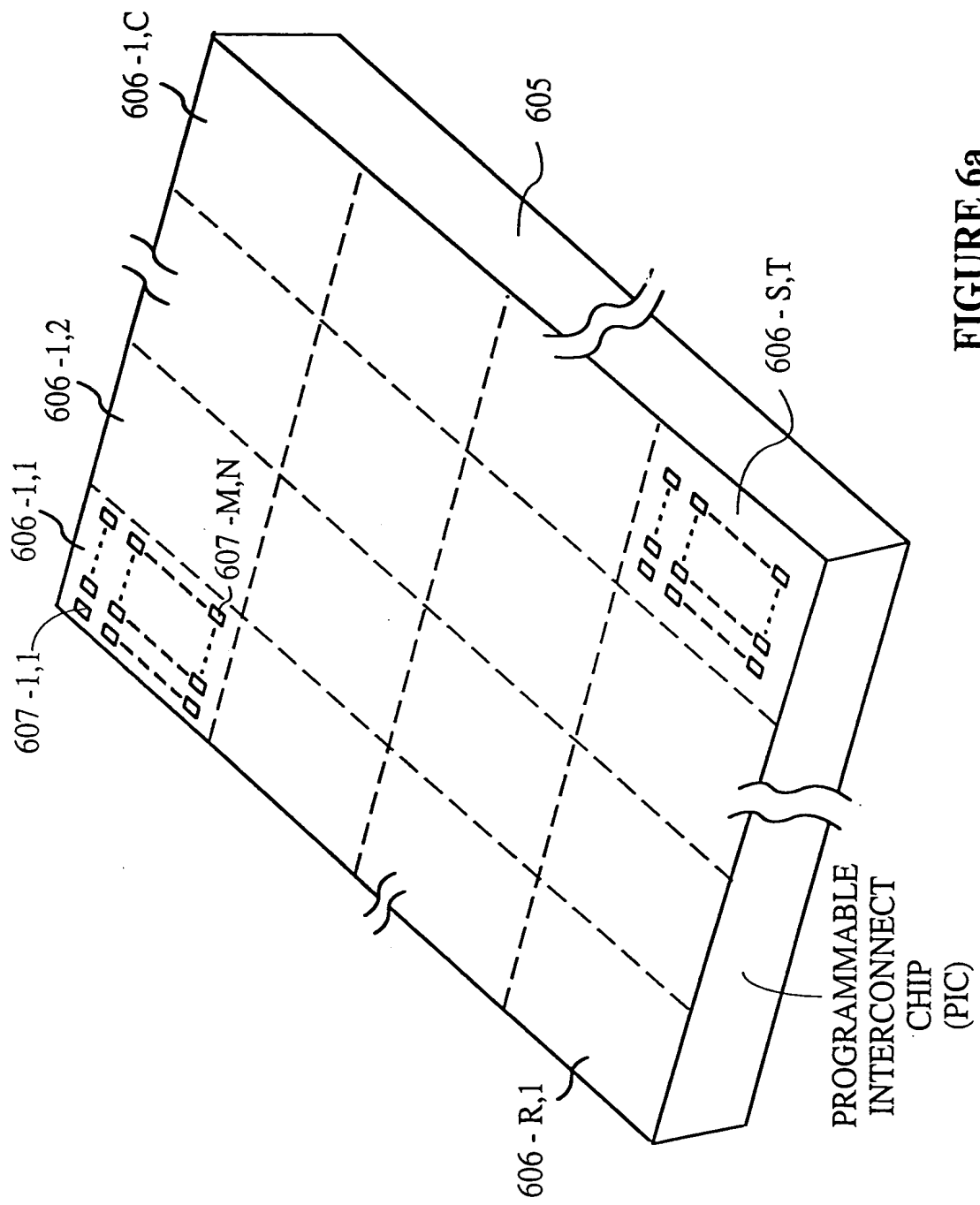
# FIELD PROGRAMMABLE PCB



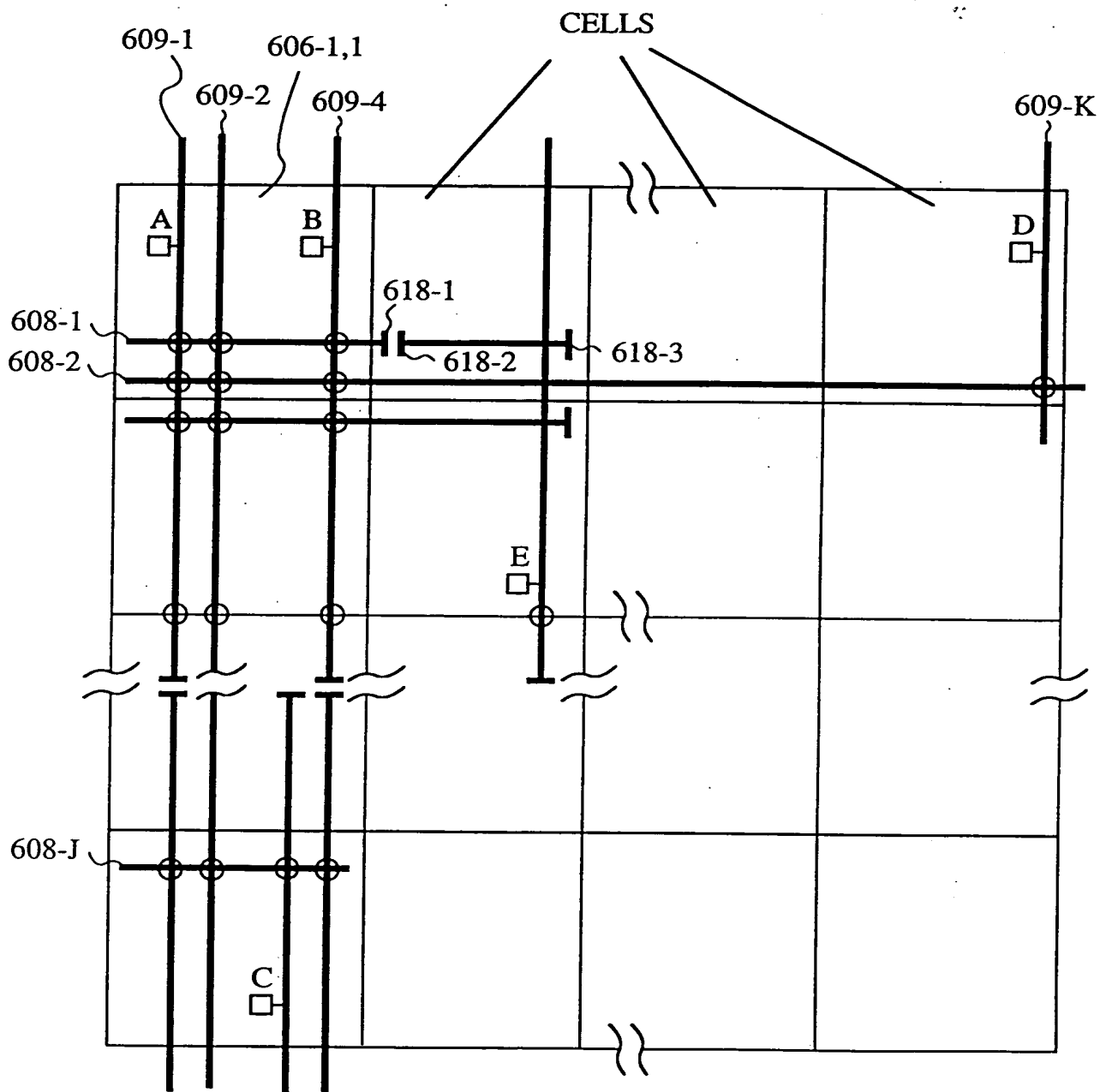
# CONTROL PORT AND TEST PORT/ DIAGNOSTIC TOOL



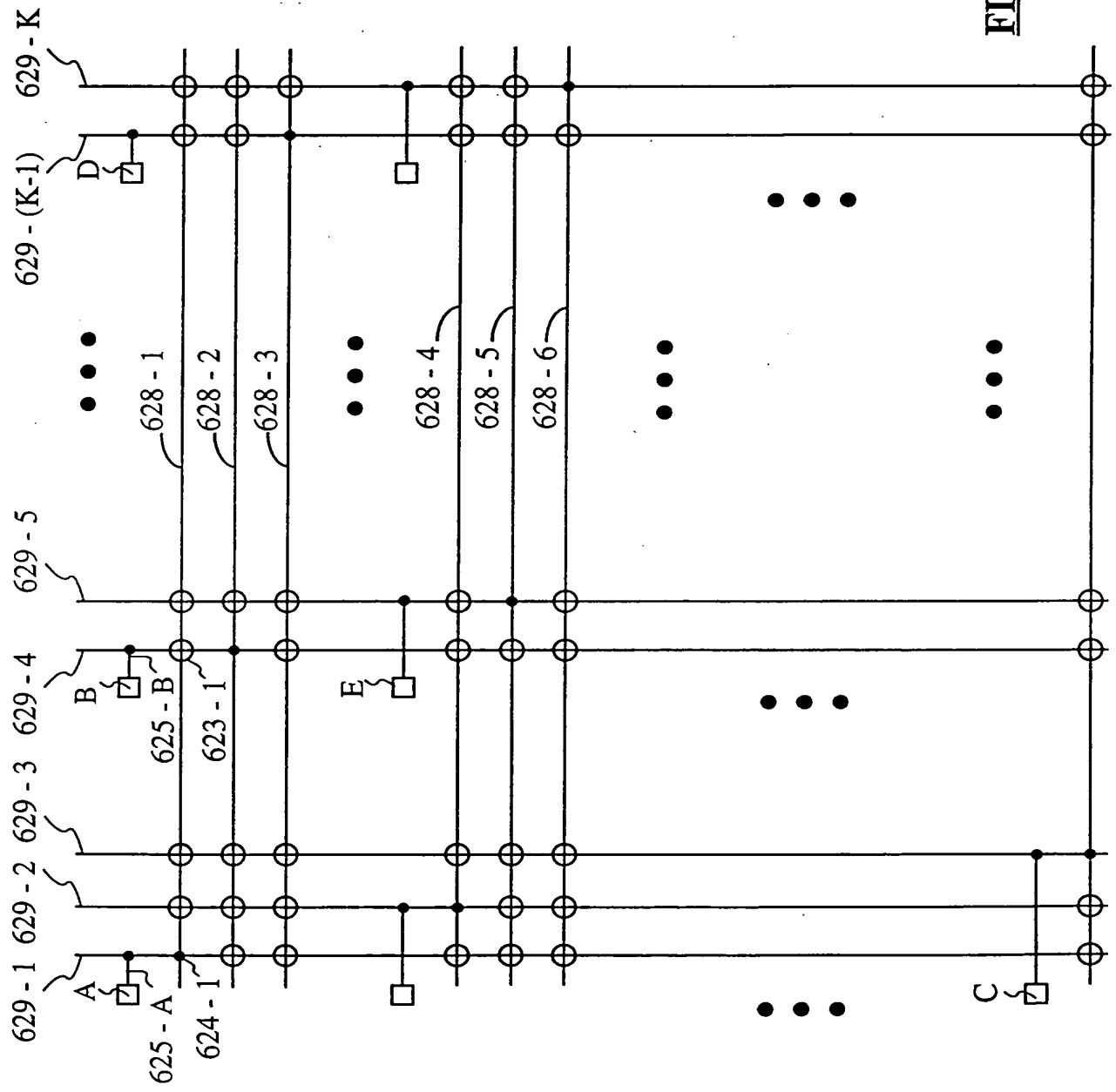
**FIGURE 5**



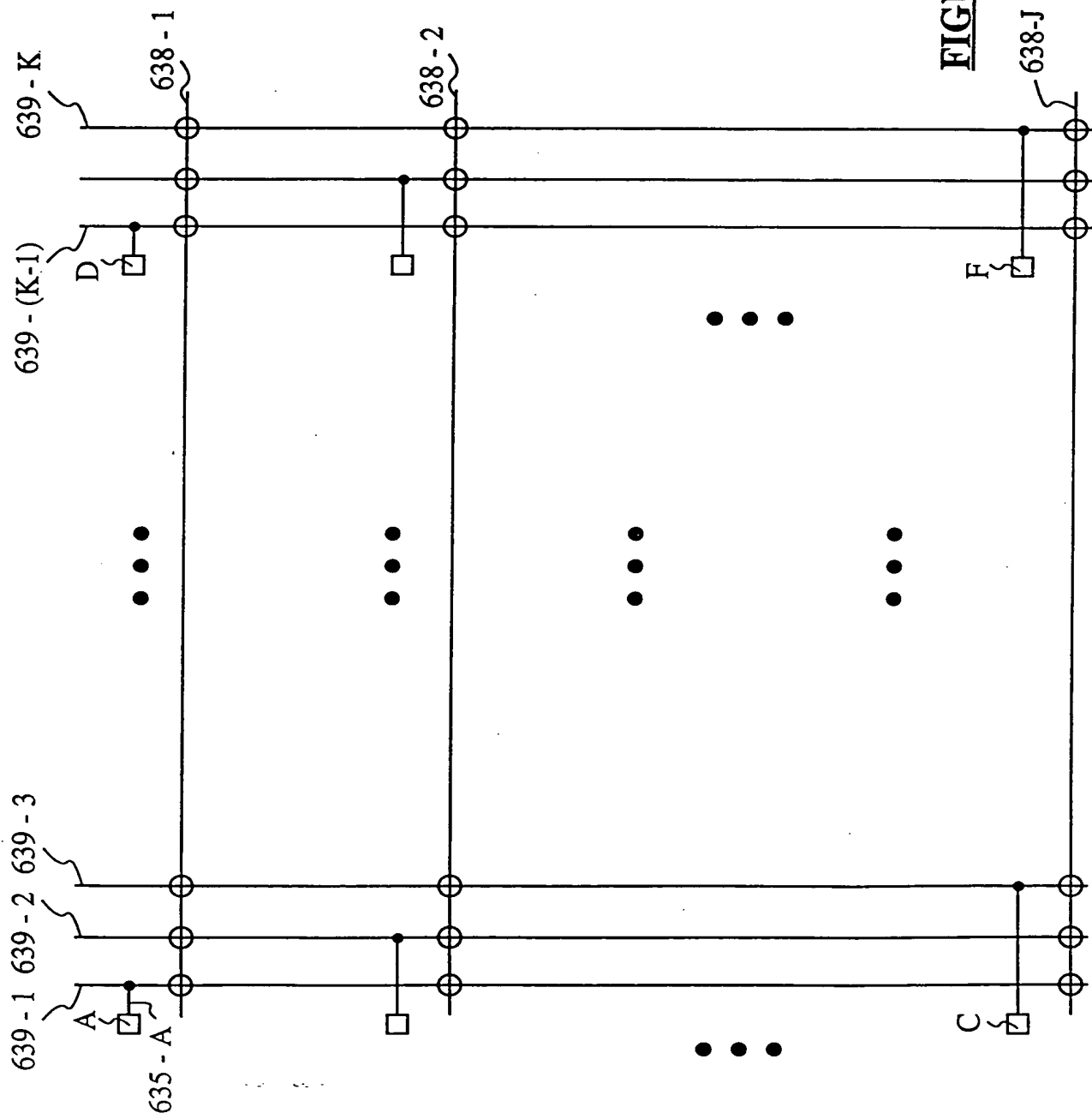
**FIGURE 6a**



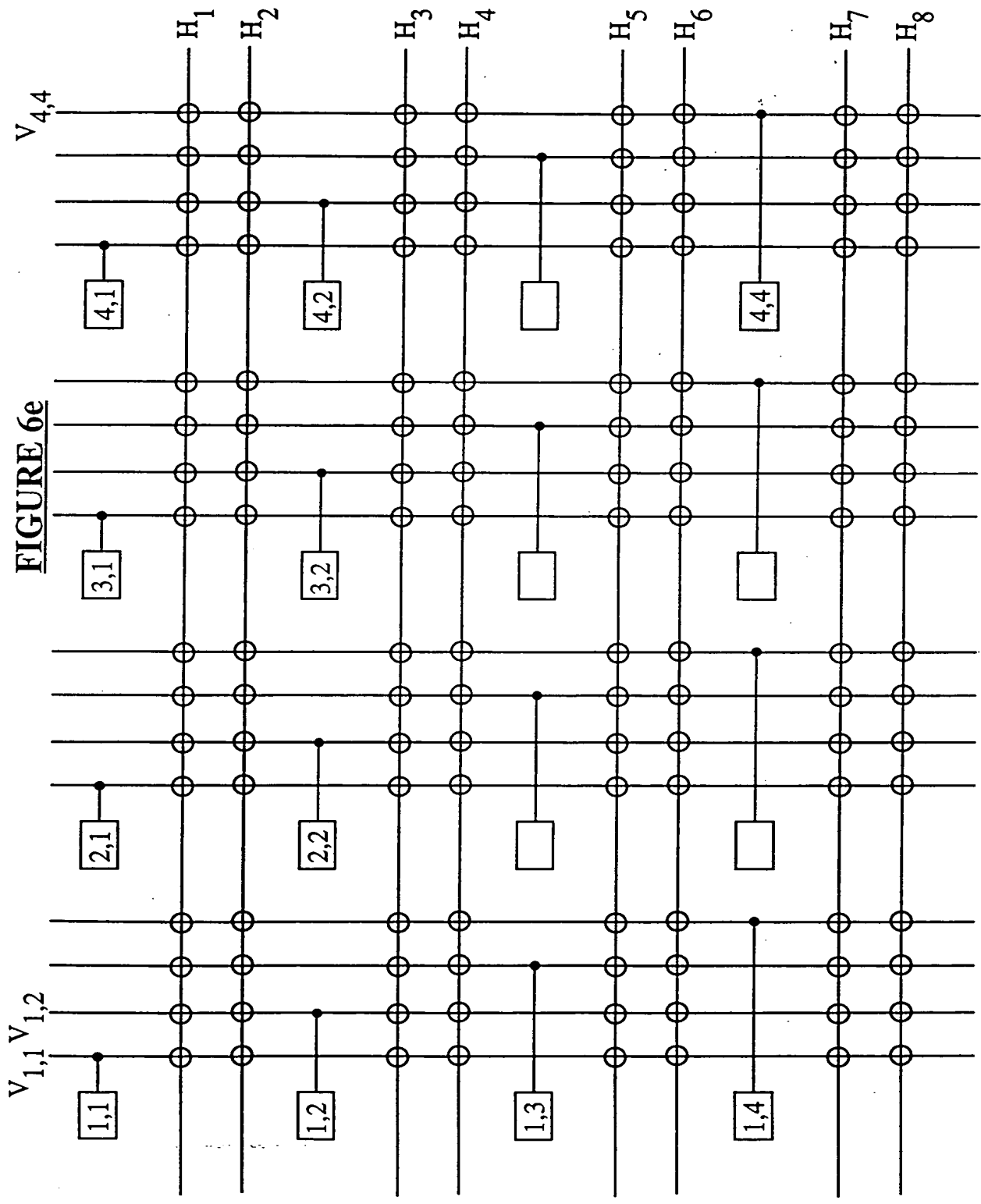
**FIG. 6b**



**FIGURE 6c**

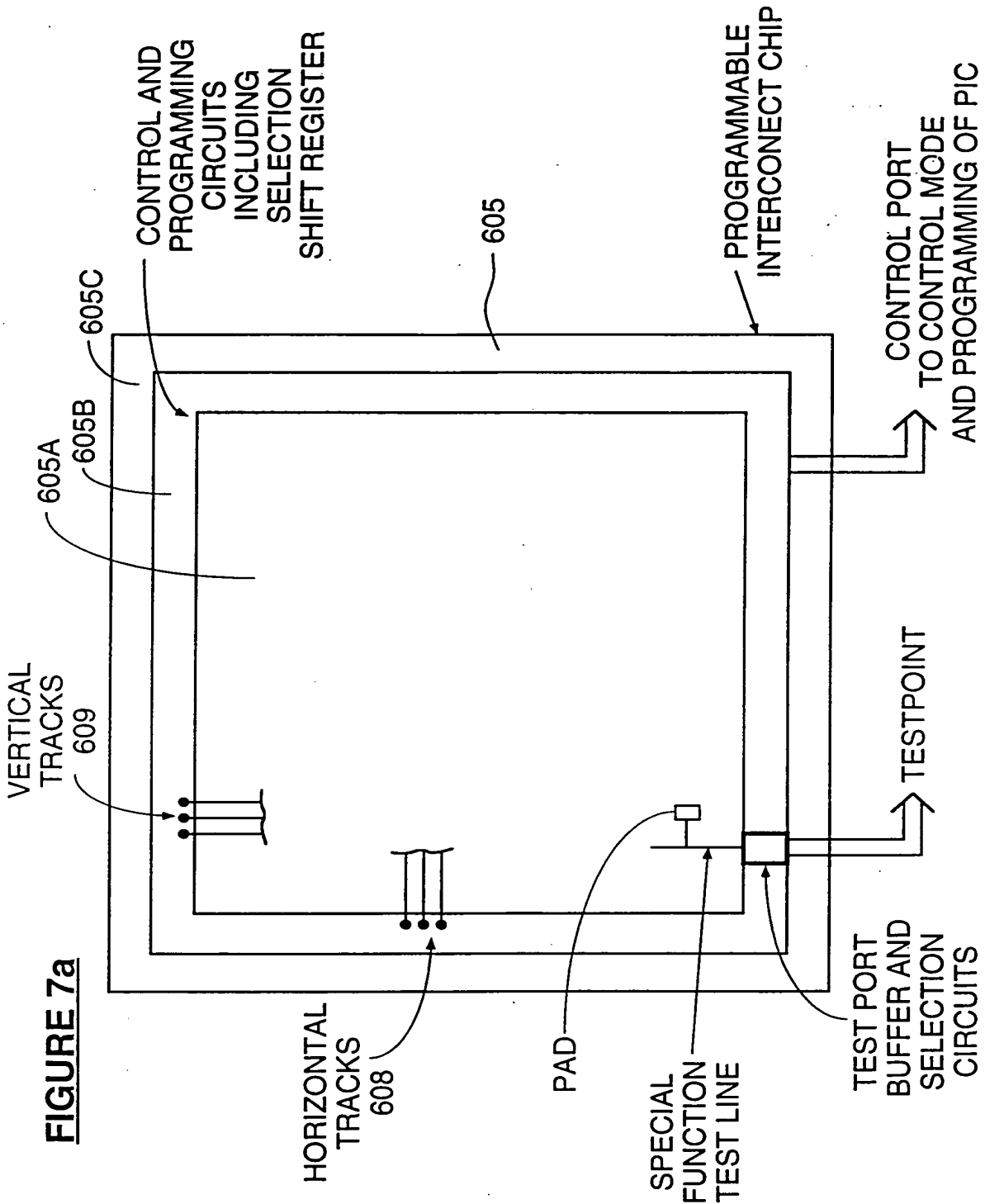


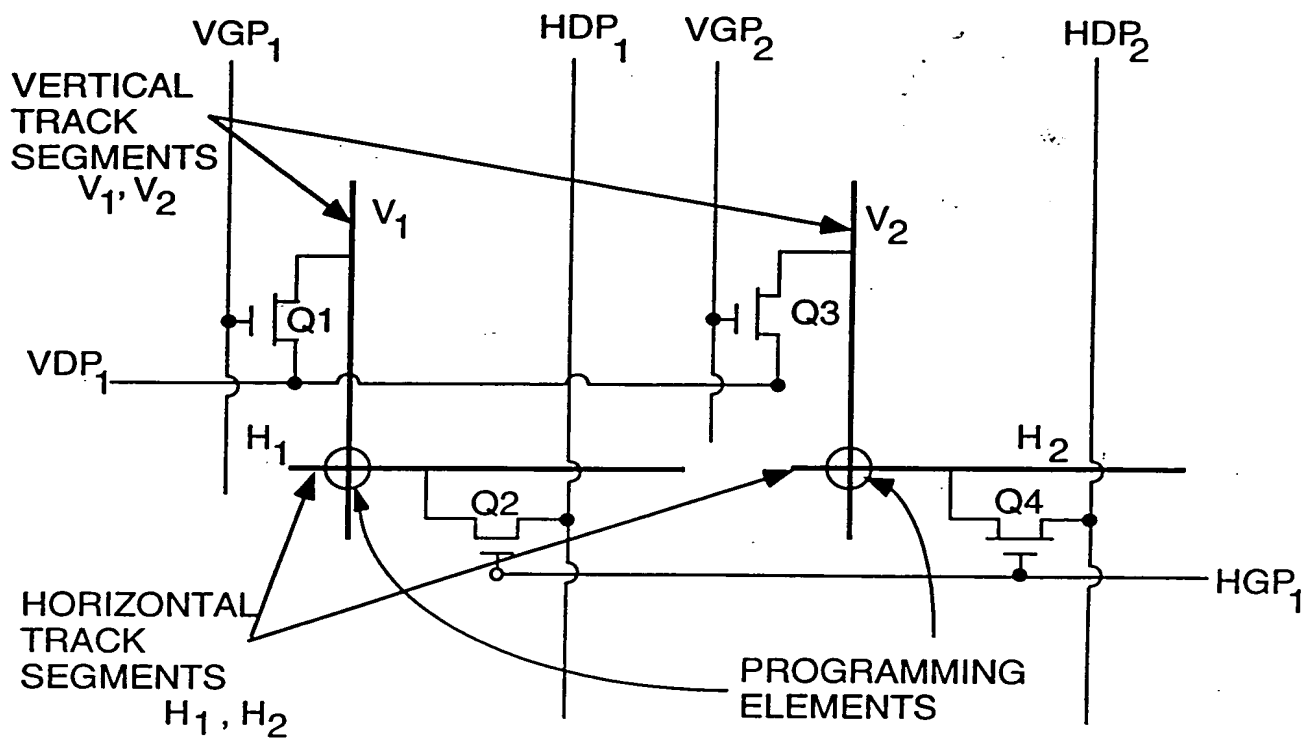
**FIGURE 6d**



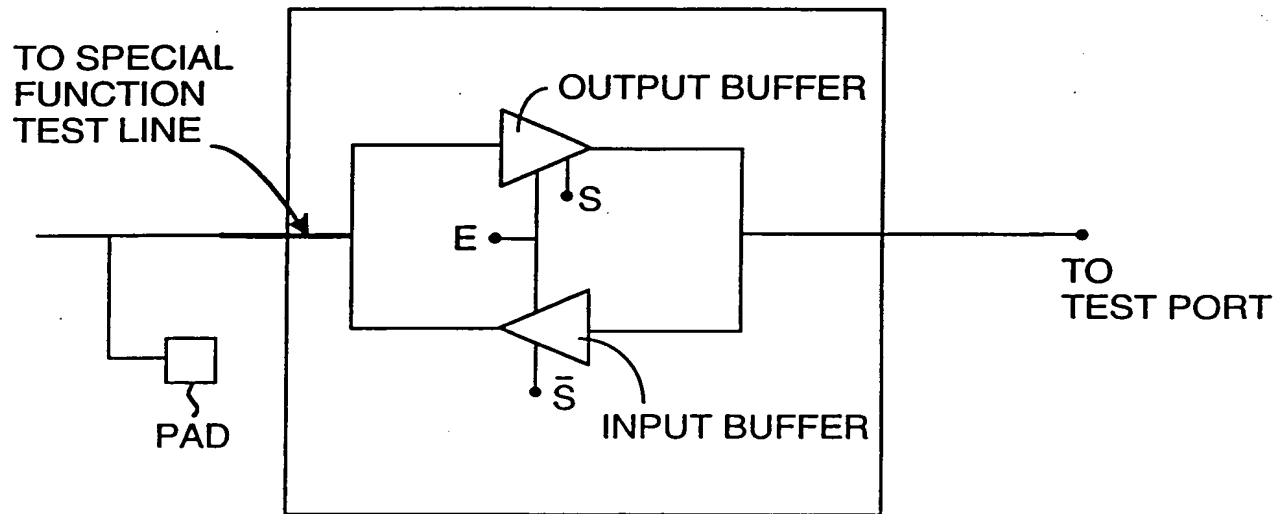
**FIGURE 6e**





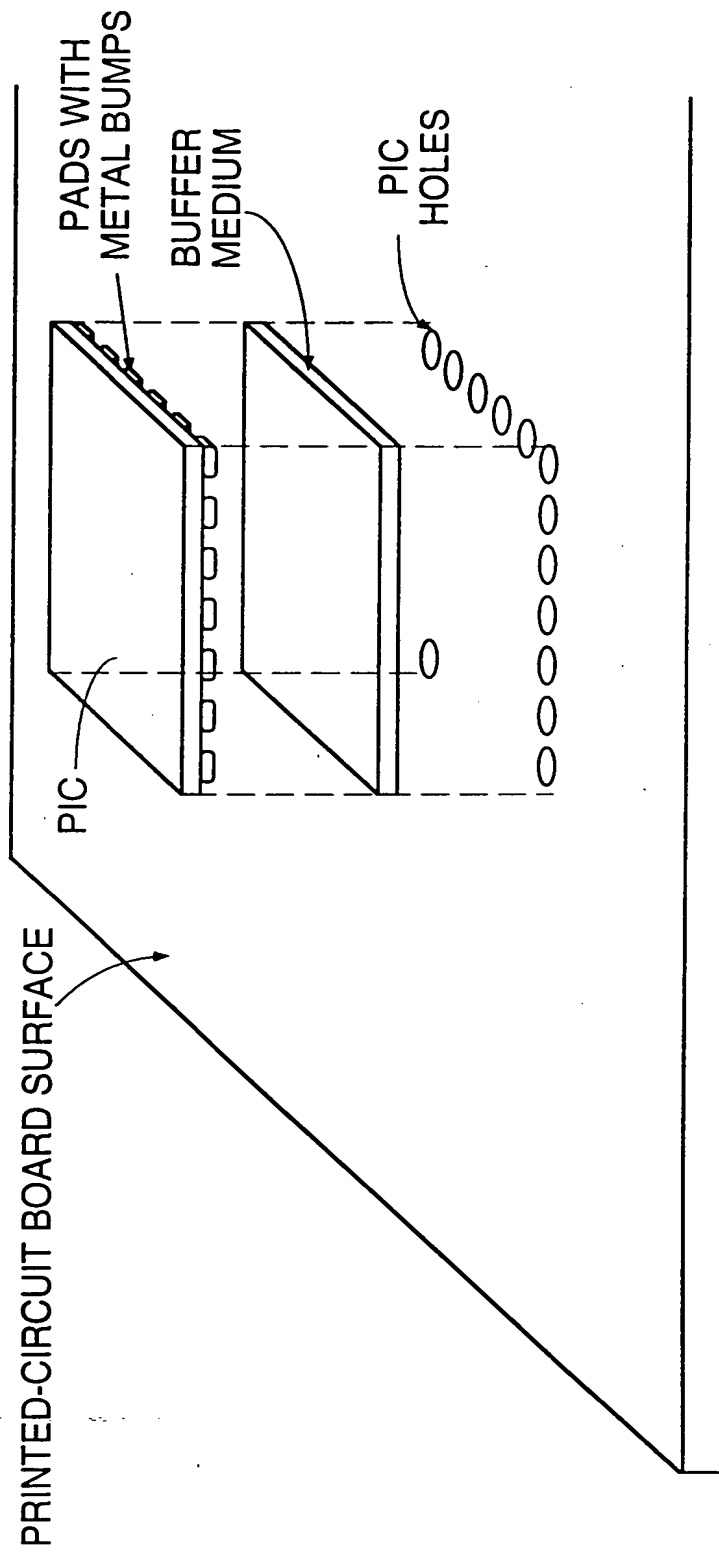


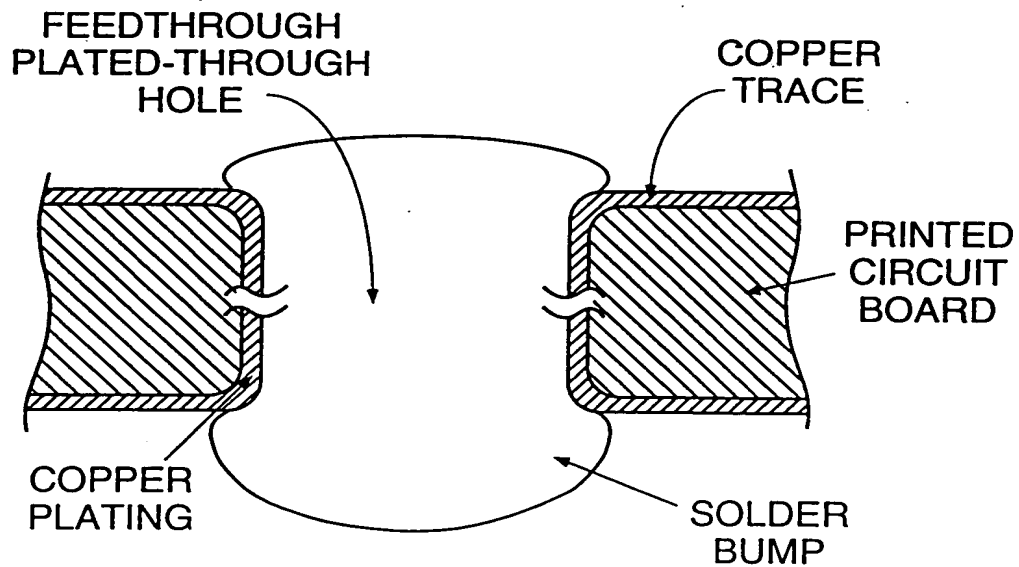
**FIGURE 7b**



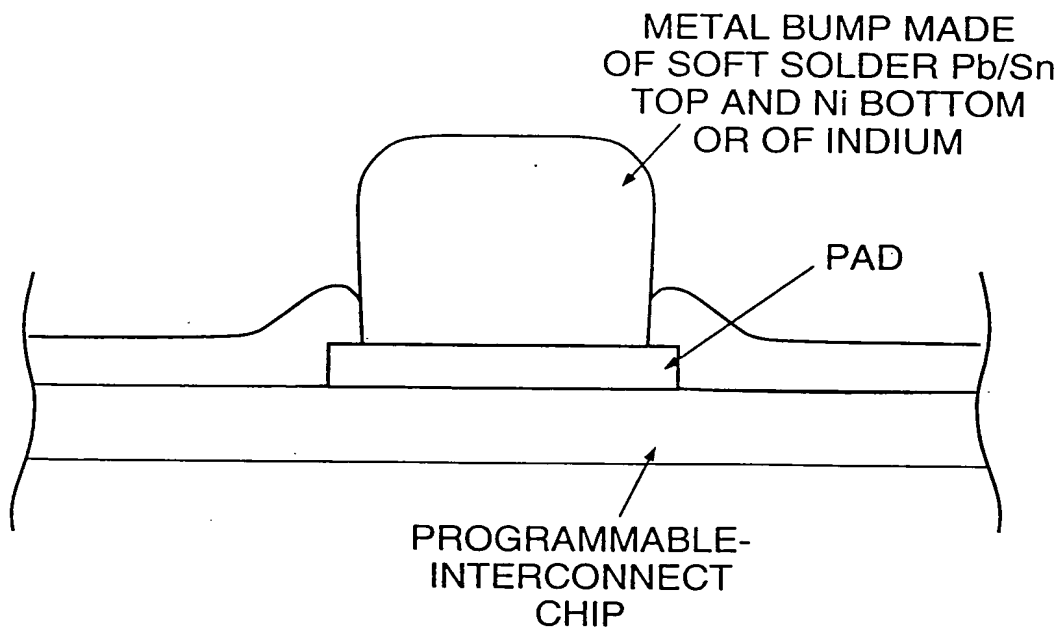
**FIGURE 7c**

**FIGURE 8a**





**FIGURE 8b1**



**FIGURE 8b2**